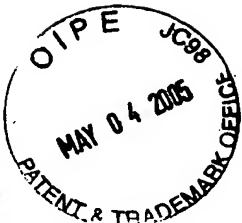


IFW



501.30278CC6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: K. KURODA et al
Serial No.: 10/694,085
Filed: October 28, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
Group: 2824
Examiner: V. Le

RESPONSE TO RESTRICTION/ELECTION REQUIREMENT

Commissioner for Patents
POB 1450
Alexandria, VA 22313-1450

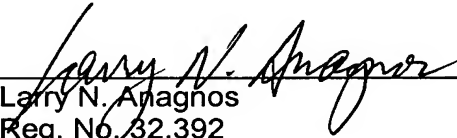
May 4, 2005

Sir:

In response to the outstanding Office Action, dated April 4, 2005, in which a Restriction Requirement was made based on a division of the claims into two separate groupings, Applicants, through their undersigned representative, hereby elect Group I (including claims 1-16), drawn to a method of trimming size and function of a semiconductor package, without traverse. Moreover, in view of the further subdivision of the Group I claims into two (2) patentably distinct species, Applicants further elect, without traverse, Species I, directed to a method of trimming a semiconductor integrated circuit having an analog circuit, and request examination therefor of claims 1-4 and 14-16, which are readable thereon.

To the extent necessary, Applicants petition for an extension of time under 37 CFR §1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including any extension of time fees, to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Dep. Acct. No. 01-2135 (501.30278CC6), and please credit any excess fees to said deposit account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP



Larry N. Anagnos
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